

# FMY67N60S1FDA

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**Automotive** FUJI POWER MOSFET

## Super J MOS<sup>®</sup> S1 series FRED type N-Channel enhancement mode power MOSFET

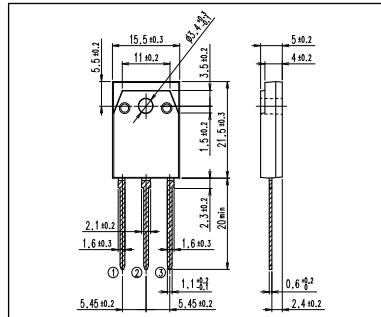
### Features

- Low on-state resistance
- Low switching loss
- Easy to use  
(more controllable switching dV/dt by Rg)
- Reliability assurance in accordance with AEC Q101
- 100% avalanche tested
- Built in fast recovery diode

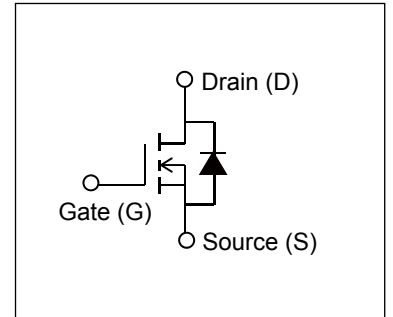
### Applications

- Automotive switching applications

### Outline Drawings [mm]



### Equivalent circuit schematic



### Absolute Maximum Ratings at T<sub>c</sub>=25°C (unless otherwise specified)

Description	Symbol	Characteristics	Unit	Remarks
Drain-Source Voltage	V <sub>DS</sub>	600	V	
	V <sub>DSX</sub>	600	V	V <sub>GS</sub> =-30V
Continuous Drain Current	I <sub>D</sub>	±67	A	T <sub>c</sub> =25°C Note*1
		±43	A	T <sub>c</sub> =100°C Note*1
Pulsed Drain Current	I <sub>DP</sub>	±201	A	
Gate-Source Voltage	V <sub>GS</sub>	±30	V	
Non-Repetitive Maximum Avalanche current	I <sub>AS</sub>	20	A	Note *2
Non-Repetitive Maximum Avalanche Energy	E <sub>AS</sub>	1293.7	mJ	Note *3
Maximum Drain-Source dV/dt	dV <sub>DS</sub> /dt	50	kV/μs	V <sub>DS</sub> =600V
Peak Diode Recovery dV/dt	dV/dt	40	kV/μs	Note *4
Peak Diode Recovery di/dt	-di/dt	100	A/μs	Note *5
Maximum Power Dissipation	P <sub>D</sub>	2.5	W	T <sub>s</sub> =25°C
		545		T <sub>c</sub> =25°C
Operating and Storage Temperature range	T <sub>ch</sub>	150	°C	
	T <sub>stg</sub>	-55 to +150	°C	

Note \*1 : Limited by maximum channel temperature.

Note \*2 : T<sub>ch</sub>≤150°C, See Fig.1 and Fig.2

Note \*3 : Starting T<sub>ch</sub>=25°C, I<sub>AS</sub>=20A, L=5.9mH, V<sub>DD</sub>=60V, R<sub>G</sub>=50Ω, See Fig.1 and Fig.2  
 E<sub>AS</sub> limited by maximum channel temperature and avalanche current.

Note \*4 : I<sub>F</sub>≤-I<sub>D</sub>, -di/dt=100A/μs, V<sub>DD</sub>≤300V, T<sub>ch</sub>≤150°C

Note \*5 : I<sub>F</sub>≤-I<sub>D</sub>, dV/dt=40kV/μs, V<sub>DD</sub>≤300V, T<sub>ch</sub>≤150°C

■ Electrical Characteristics at T<sub>c</sub>=25°C (unless otherwise specified)

• Static Ratings

Description	Symbol	Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	I <sub>D</sub> =1mA V <sub>GS</sub> =0V	600	-	-	V
Gate Threshold Voltage	V <sub>GS(th)</sub>	I <sub>D</sub> =3.5mA V <sub>DS</sub> =V <sub>GS</sub>	3.0	4.0	5.0	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =600V V <sub>GS</sub> =0V	-	-	3	μA
		V <sub>DS</sub> =480V V <sub>GS</sub> =0V	-	-	2	mA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±30V V <sub>DS</sub> =0V	-	-	100	nA
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	I <sub>D</sub> =33.5A V <sub>GS</sub> =10V	-	35	42	mΩ
Gate- Resistance	R <sub>G</sub>	f=1MHz, Open drain	-	1.3	-	Ω

• Dynamic Ratings

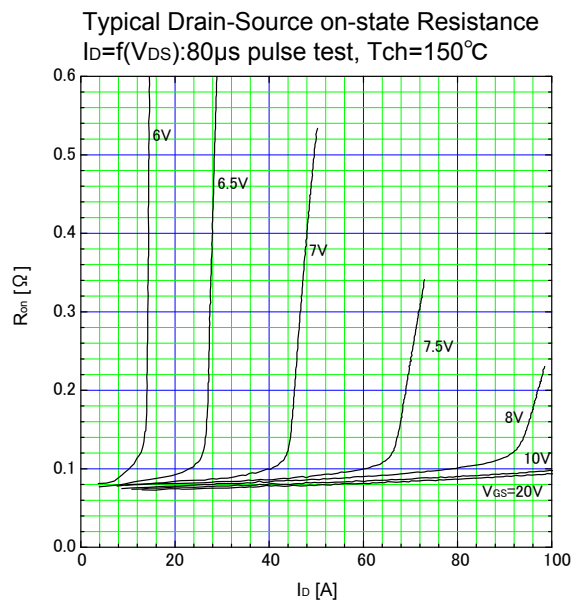
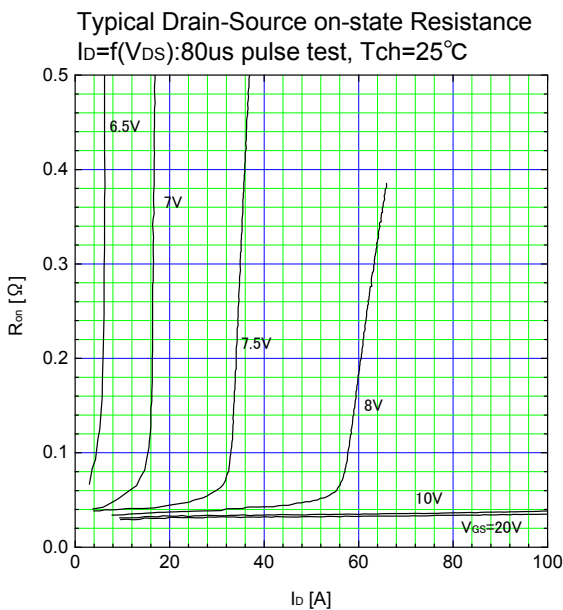
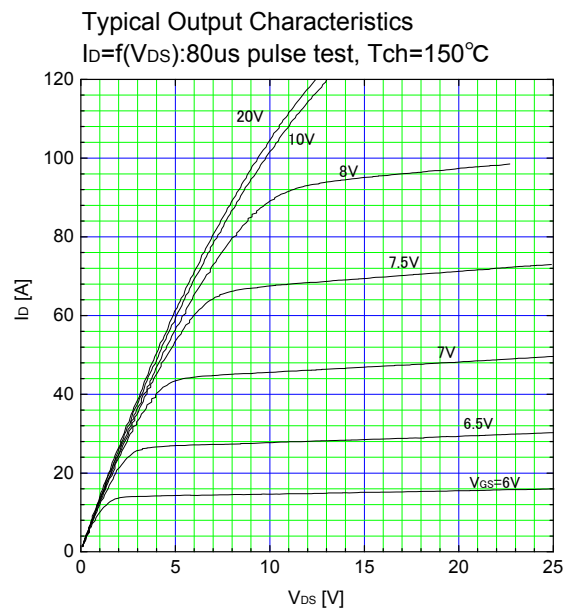
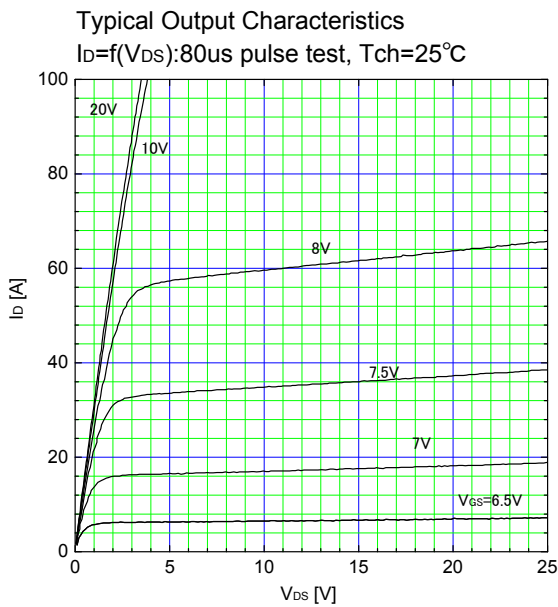
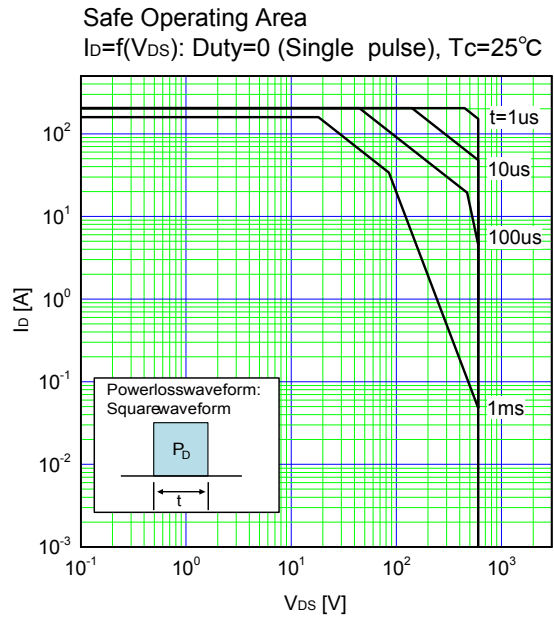
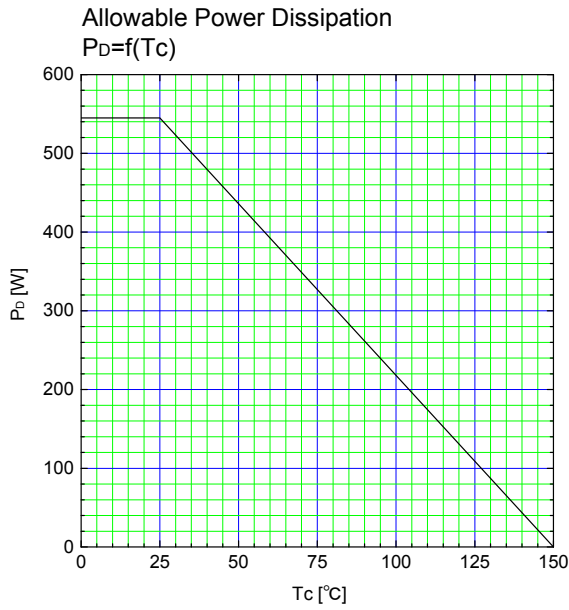
Description	Symbol	Conditions	Min.	Typ.	Max.	Unit
Forward Transconductance	g <sub>fs</sub>	I <sub>D</sub> =33.5A V <sub>DS</sub> =10V	16	-	-	S
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =400V V <sub>GS</sub> =0V	-	4900	-	pF
Output Capacitance	C <sub>oss</sub>	f=250kHz	-	173	-	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	14	-	
Turn-On Time	t <sub>d(on)</sub>	V <sub>DD</sub> =400V, V <sub>GS</sub> =10V I <sub>D</sub> =33.5A, R <sub>G</sub> =10Ω See Fig.3 and Fig.4	-	175	-	ns
	t <sub>r</sub>		-	47	-	
Turn-Off Time	t <sub>d(off)</sub>		-	210	-	
	t <sub>f</sub>		-	22	-	
Total Gate Charge	Q <sub>G</sub>	V <sub>DD</sub> =480V, I <sub>D</sub> =67A	-	220	-	nC
Gate-Source Charge	Q <sub>GS</sub>	V <sub>GS</sub> =10V	-	55	-	
Gate-Drain Charge	Q <sub>GD</sub>	See Fig.5	-	135	-	

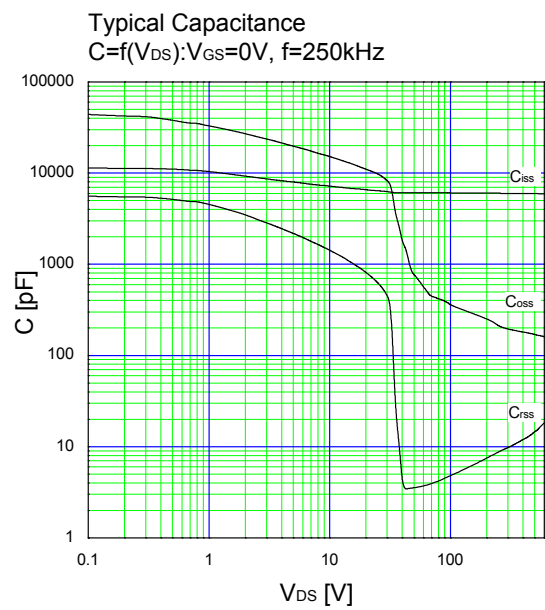
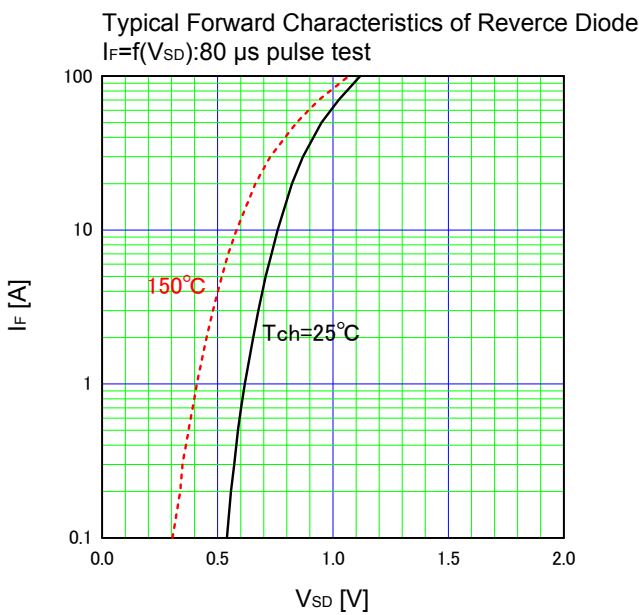
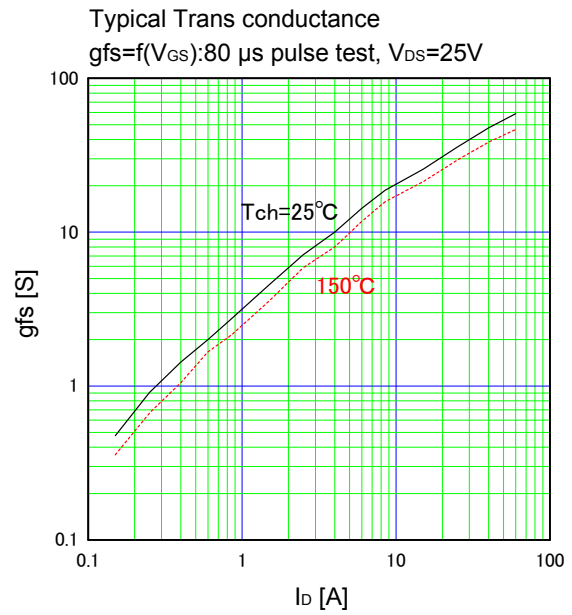
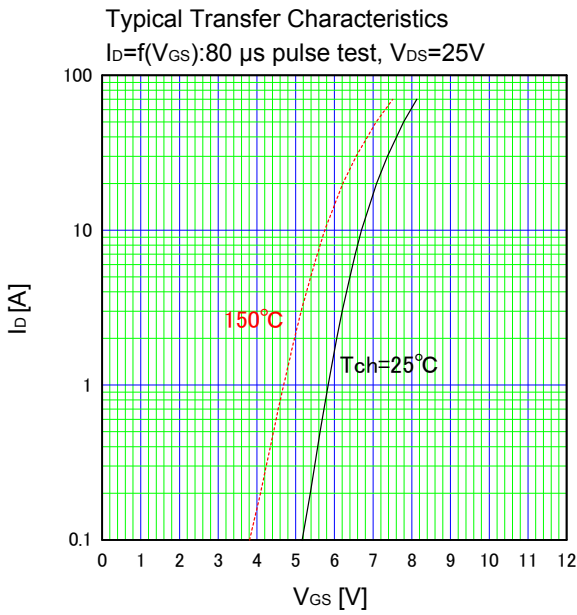
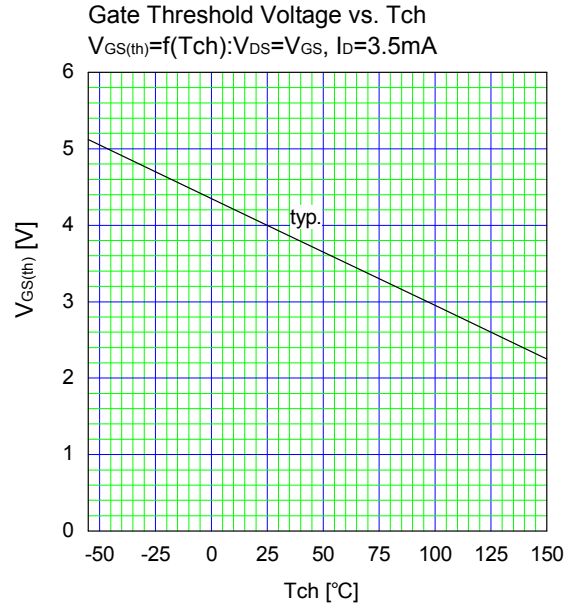
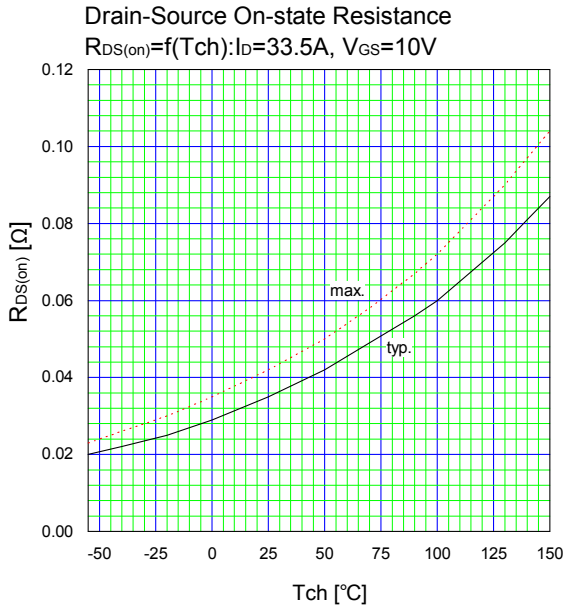
• Reverse Ratings

Description	Symbol	Conditions	Min.	Typ.	Max.	Unit
Avalanche Capability	I <sub>AV</sub>	L=5.9mH, T <sub>ch</sub> =25°C See Fig.1 and Fig.2	20	-	-	A
Diode Forward On-Voltage	V <sub>SD</sub>	I <sub>F</sub> =67A, V <sub>GS</sub> =0V T <sub>ch</sub> =25°C	-	-	1.35	V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> =43A, V <sub>GS</sub> =0V V <sub>DD</sub> =300V	-	250	-	ns
Reverse Recovery Charge	Q <sub>rr</sub>	-di/dt=100A/μs See Fig.6	-	2.7	-	μC

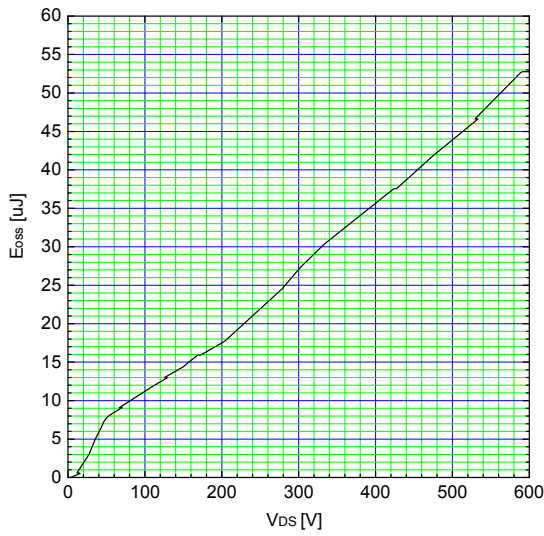
■ Thermal Characteristics

Description	Symbol	Min.	Typ.	Max.	Unit
Channel to Case	R <sub>th(ch-c)</sub>	-	-	0.23	°C/W
Channel to Ambient	R <sub>th(ch-a)</sub>	-	-	50	°C/W

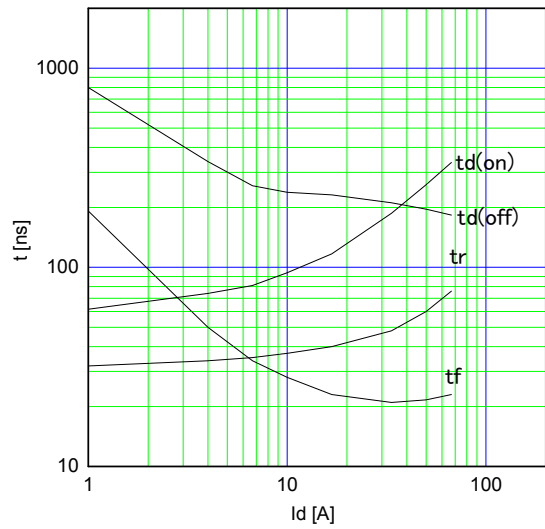




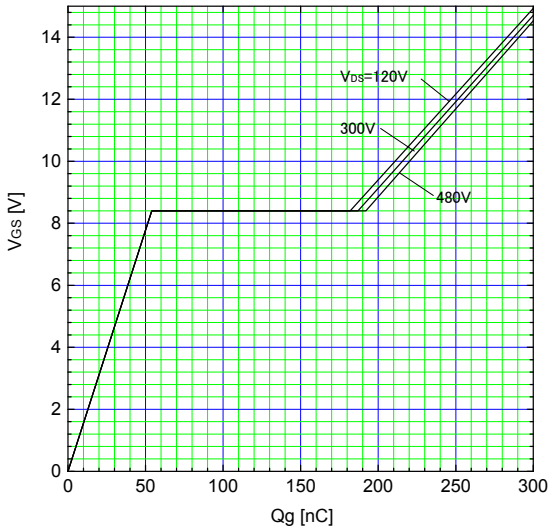
Typical Cross stored energy



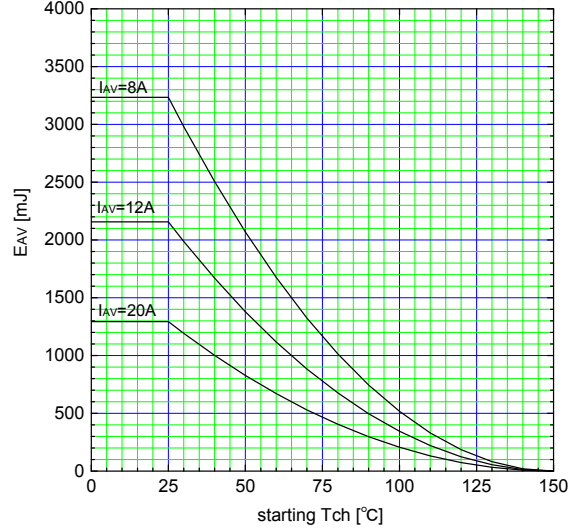
Typical Switching Characteristics vs.  $I_D$   $T_{ch}=25^\circ C$   
 $t=f(I_D): V_{DD}=400V, V_{GS}=10V/0V, R_G=10\Omega$



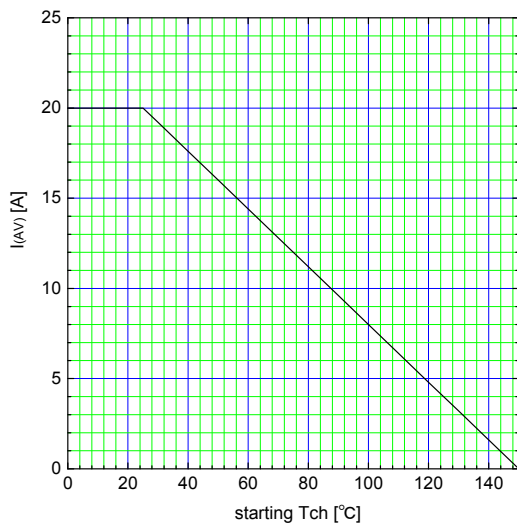
Typical Gate Characteristics  
 $V_{GS}=f(Q_g): I_D=67A, T_{ch}=25^\circ C$



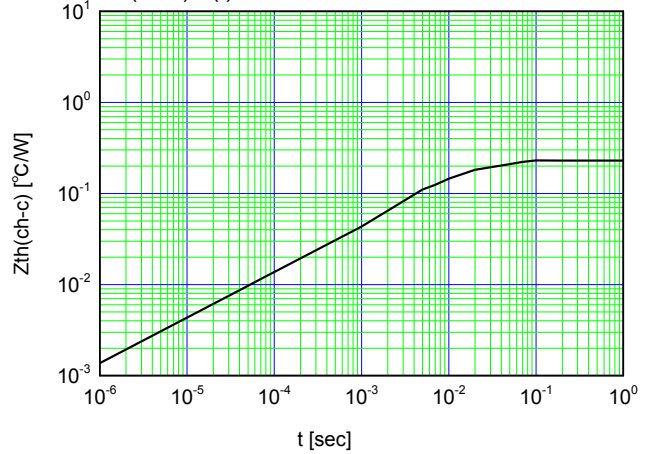
Maximum Avalanche Energy vs. starting  $T_{ch}$   
 $E_{(AV)}=f(\text{starting } T_{ch}), V_{CC}=60V, I_{(AV)} \leq 20A$



Maximum Avalanche Current vs. starting  $T_{ch}$   
 $I_{(AV)}=f(\text{starting } T_{ch}), \text{single pulse}$



Transient Thermal Impedance  
 $Z_{th(ch-c)}=f(t): D=0$



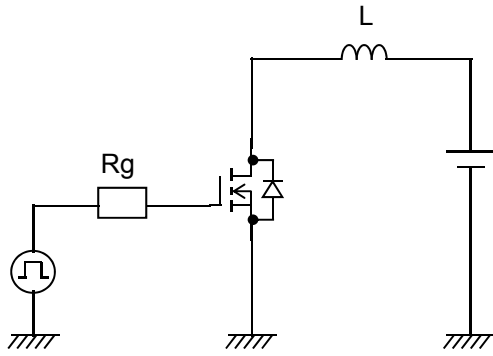


Fig.1 Avalanche Test circuit

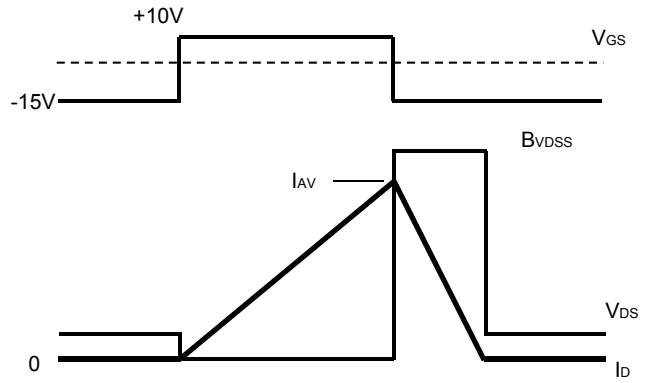


Fig.2 Operating waveforms of Avalanche Test

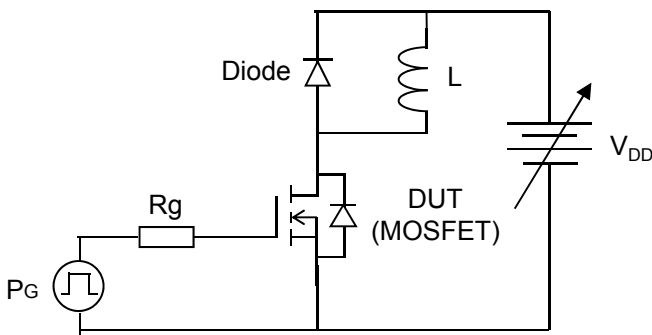


Fig.3 Switching Test circuit

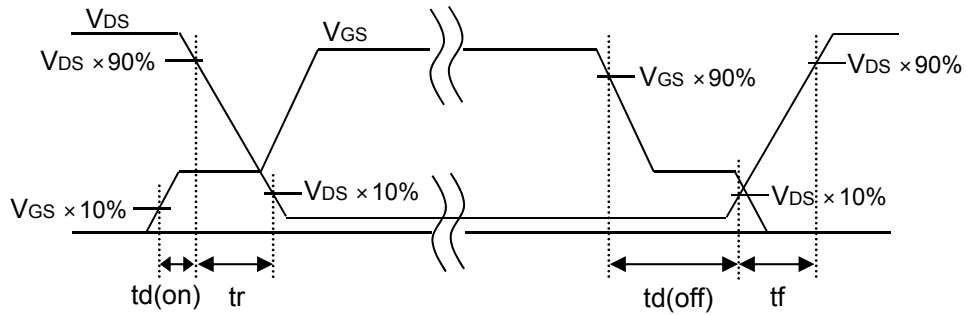


Fig.4 Operating waveform of Switching Test

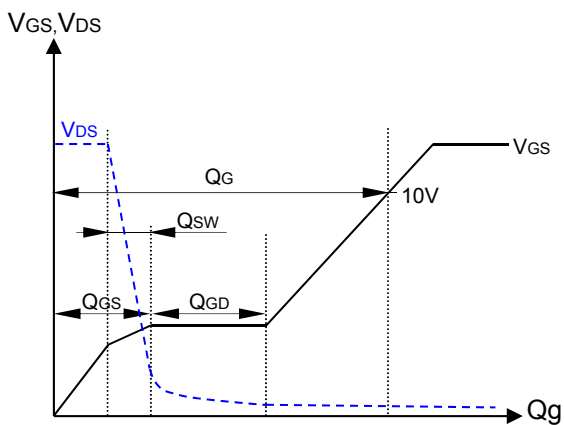


Fig.5 Operating waveform of Gate charge Test

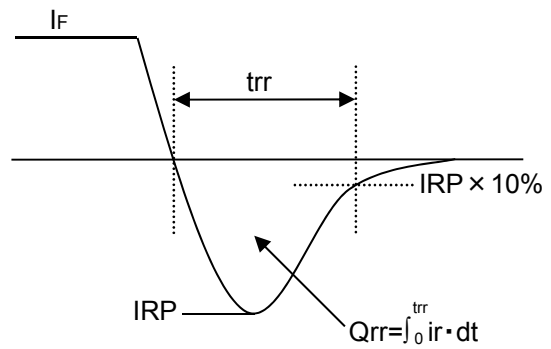
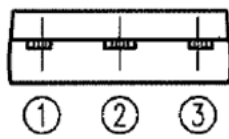
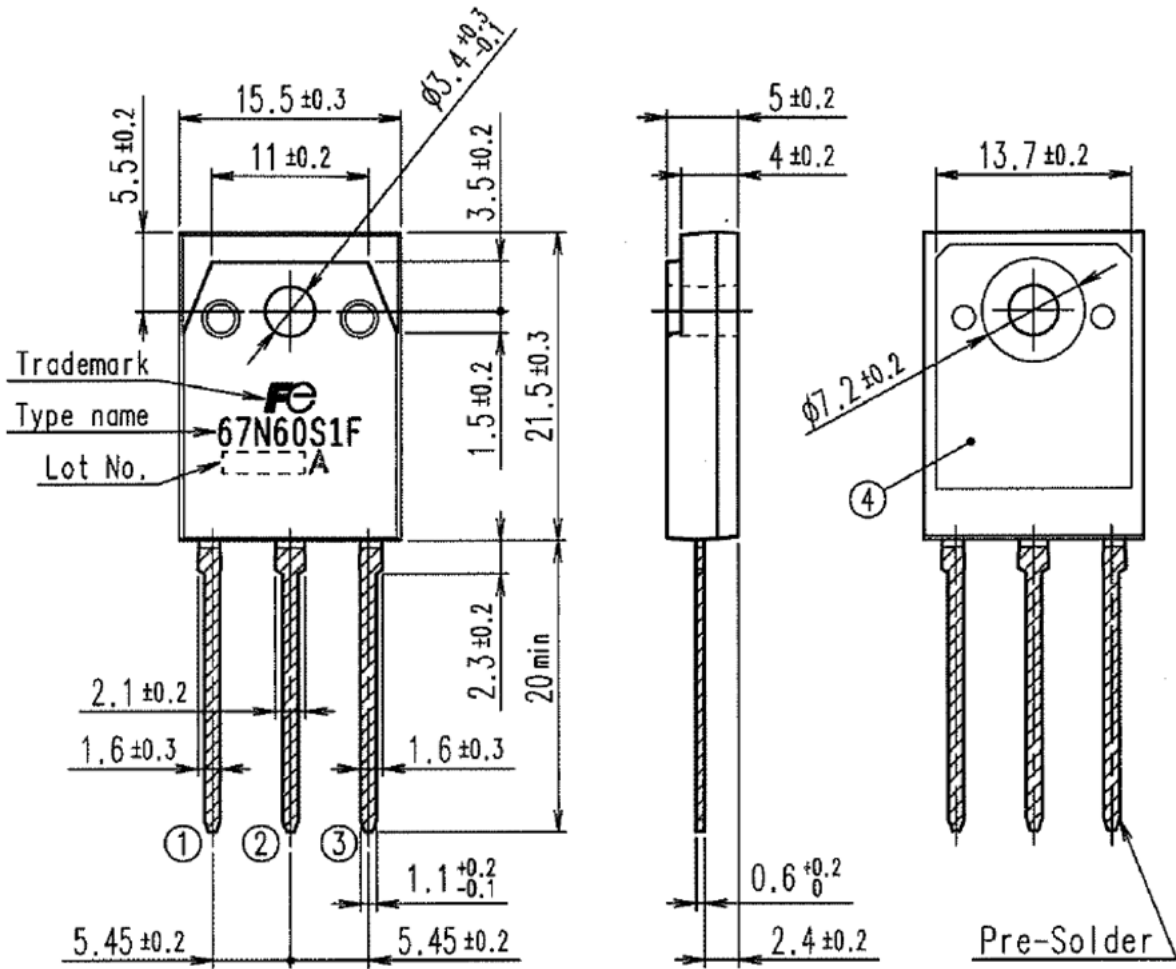


Fig.6 Operating waveform of Body diode Recovery Test

■ Overview



CONNECTION

- ① GATE
- ② ④ DRAIN
- ③ SOURCE

JEDEC: TO-247

DIMENSIONS ARE IN MILLIMETERS.

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