FMH47N60S1

http://www.fujielectric.com/products/semiconductor/

FUJI POWER MOSFET

Super J MOS® S1 series

N-Channel enhancement mode power MOSFET

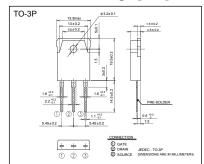
Features

Pb-free lead terminal RoHS compliant

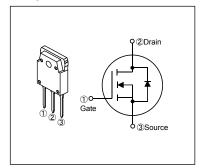
Applications

For switching

Outline Drawings [mm]



Equivalent circuit schematic



■ Absolute Maximum Ratings at T_c=25°C (unless otherwise specified)

Description	Symbol	Characteristics	Unit	Remarks
Drain Source Voltage	V _{DS}	600	V	
Drain-Source Voltage	V _{DSX}	600	V	V _{GS} =-30V
Continuous Drain Current		±47	Α	Tc=25°C Note*1
Continuous Drain Current	ID RA	TK公至29773 訂	Α	Tc=100°C Note*1
Pulsed Drain Current	lop/	3 ±140 5 5	A	
Gate-Source Voltage	Ves	1 £30	V	
Repetitive and Non-Repetitive Maximum Avalanche Current	IAR	9.5	Α	Note *2
Non-Repetitive Maximum Avalanche Energy	EAS \	J\1267@\\	mJ	Note *3
Maximum Drain-Source dV/dt	dVps/dt	000 50	∠ ∖ kV/μs	V _{DS} ≤ 600V
Peak Diode Recovery dV/dt	dyddtce 123	13+-1,5	9 ° ∖kV/µs	Note *4
Peak Diode Recovery -di/dt	di/dt +	願い80	A/µs	Note *5
THE MENTINGER	Diately 270	anin C2.5	W	T _a =25°C
Maximum Power Dissipation	Tex Los des	390	VV	Tc=25°C
On and Starting and Starting Towns of the Starting and St	Mentor Hear	150	°C	
Peak Diode Recovery - di/dt Peak Diode Recovery - di/dt Maximum Power Dissipation Operating and Storage Temperature range	T _{stg}	-55 to +150	°C	

Note *1 : Limited by maximum channel temperature.

Note *2 : T_{ch}≤150°C, See Fig.1 and Fig.2 Note *3 : Starting T_{ch}=25°C, I_{AS}=7.6A, L=40.2mH, V_{DD}=60V, R_G=50Ω, See Fig.1 and Fig.2 E_{AS} limited by maximum channel temperature and avalanche current.

Note *4 : I₅≤-23.5A, -di/dt=80A/µs, V₀₀≤300V, T₀h≤150°C. Note *5 : I₅≤-23.5A, dV/dt=13kV/µs, V₀₀≤300V, Tℴh≤150°C.

■ Electrical Characteristics at T_c=25°C (unless otherwise specified)

Static Ratings

Description	Symbol	Conditions		min.	typ.	max.	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	I _D =250μA V _{GS} =0V		600	-	-	V
Gate Threshold Voltage	V _{GS(th)}	I _D =250µA V _{DS} =V _{GS}		2.5	3.0	3.5	V
Zero Gate Voltage Drain Current	loss	V _{DS} =600V V _{GS} =0V	T _{ch} =25°C	-	-	25	μΑ
		V _{DS} =480V V _{GS} =0V	T _{ch} =125°C	-	-	250	
Gate-Source Leakage Current	I _{GSS}	V _{GS} = ±30V V _{DS} =0V		-	10	100	nA
Drain-Source On-State Resistance	R _{DS(on)}	I _D =23.5A V _{GS} =10V		-	0.059	0.07	Ω
Gate resistance	R _G	f=1MHz, open drain		-	1.1	-	Ω

• Dynamic Ratings

Description	Symbol	Conditions	min.	typ.	max.	Unit
Forward Transconductance	g fs	I _D =23.5A V _{DS} =25V	19	38	-	S
Input Capacitance	Ciss	V _{DS} =10V	-	4000	-	
Output Capacitance	Coss	V _{GS} =0V	-	8400	-	
Reverse Transfer Capacitance	Crss	f=1MHz	-	770	-	
Effective output capacitance, energy related (Note *6)	C _{o(er)}	V _{GS} =0V V _{DS} =0480V	-	210	-	pF
Effective output capacitance, time related (Note *7)	C _{o(tr)}	V _{GS} =0V V _{DS} =0480V ID=constant	-	790	-	
Turn-On Time	t _{d(on)}		-	36	-	
Turn-On Time	t r	V_{DD} =400V, V_{GS} =10V I_{D} =23.5A, R_{G} =8.2 Ω	-	83	-	ns
Turn-Off Time	t _{d(off)}	See Fig.3 and Fig.4	-	135	-	
	t _f		-	17	-	
Total Gate Charge	Q _G		-	125	-	
Gate-Source Charge	Q _{GS}	V _{DD} =480V, I _D =47A	-	29	-	nC
Gate-Drain Charge	Q _{GD}	− V _{ss} =10V _ See Fig.5	-	46	-	IIC
Drain-Source crossover Charge	Qsw	3331.19.3	-	18	-	

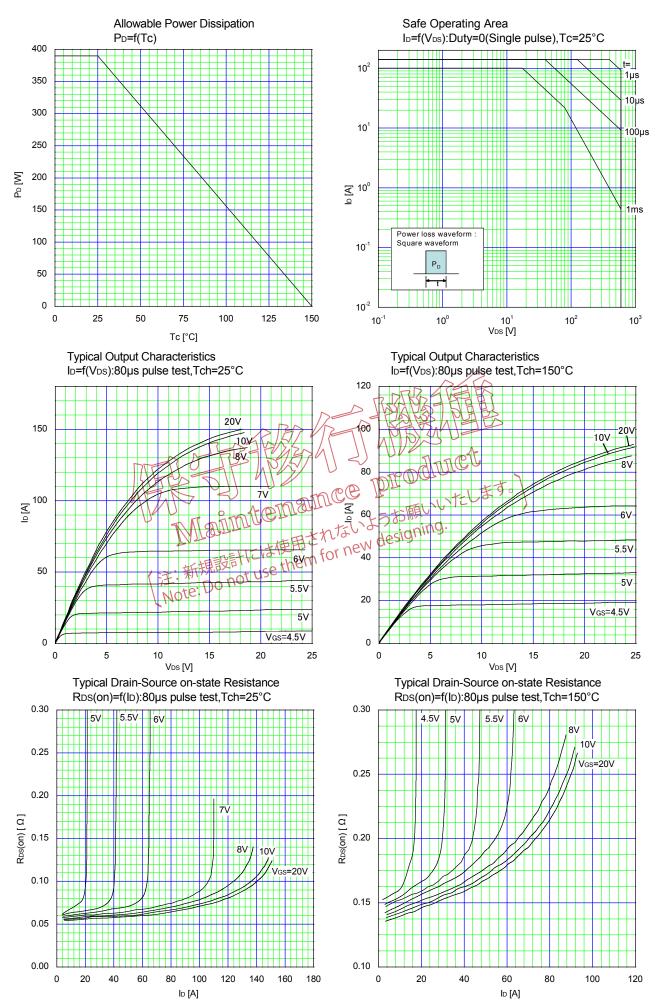
Note *6 : $C_{o(er)}$ is a fixed capacitance that gives the same stored energy as C_{oss} while V_{DS} is rising from 0 to 80% BV_{DSS}. Note *7 : $C_{o(er)}$ is a fixed capacitance that gives the same charging times as C_{oss} while V_{DS} is rising from 0 to 80% BV_{DSS}.

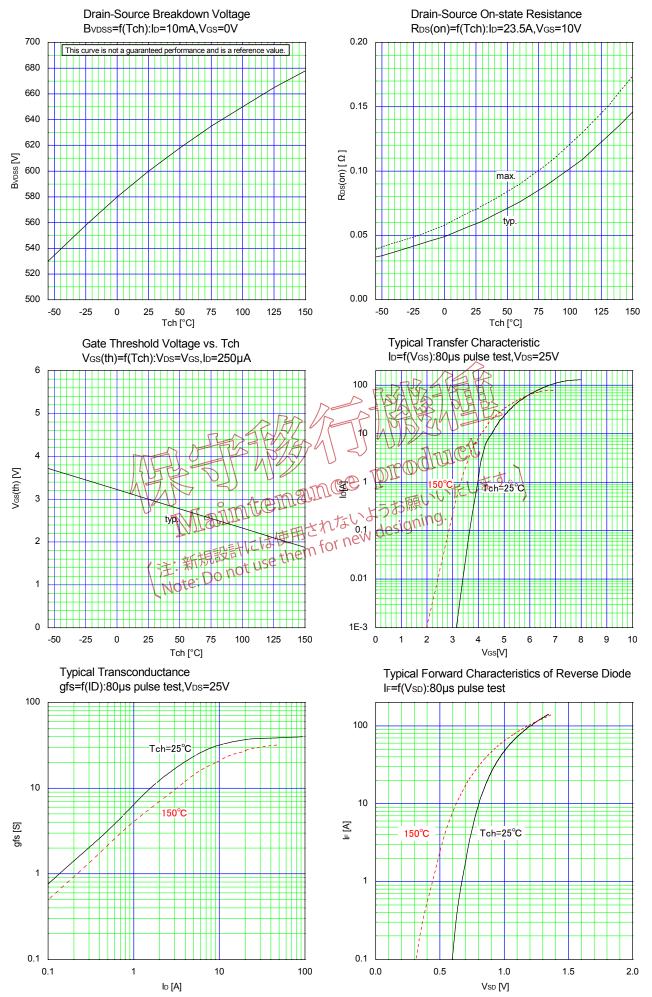
Reverse Diode

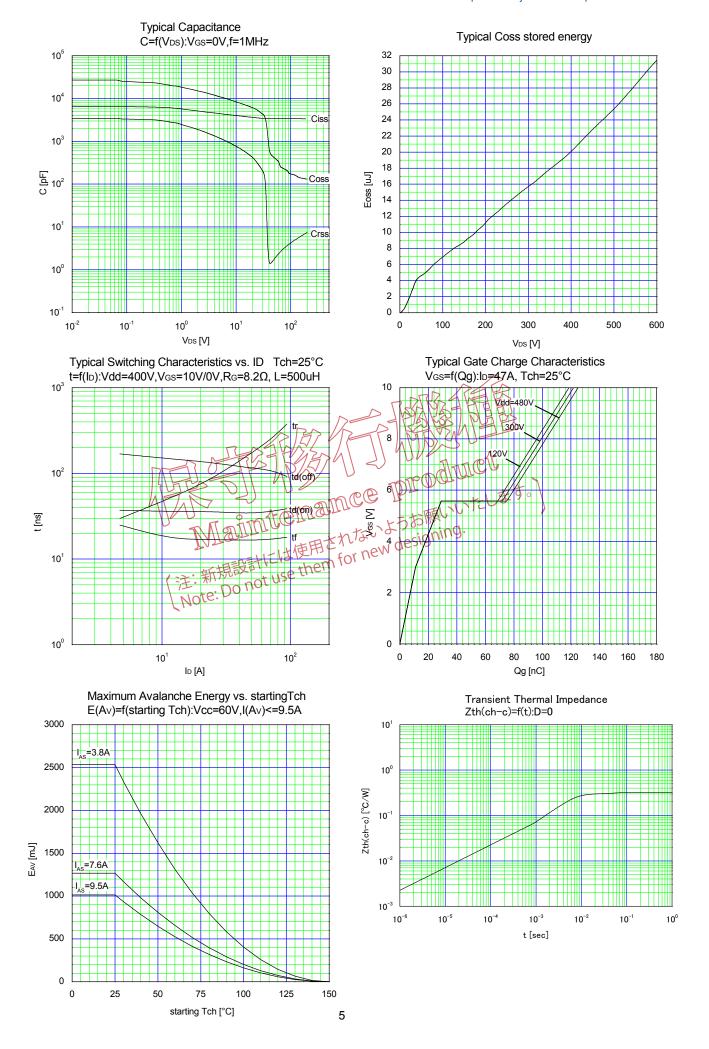
Reverse Diode			D DTR		R		
Description	Symbol	Conditions	12-0-12-0-12-0-12-0-12-0-12-0-12-0-12-0	E-rain &	typ.	max.	Unit
Avalanche Capability	lav T	L=20.6mH, Tor=2		9.5	-	-	Α
Diode Forward On-Voltage	TV-S	T _{ch} =25°C	DILO(JIMIC A	1.0	1.35	٧
Reverse Recovery Time	t _m	1-=23.5A Ves=0	からかり	untel	470	-	ns
Reverse Recovery Charge	o Mist	-di/dt=80A/us -di/dt=80A/us -di/dt=80A/us	されない。design	iua.	8.7	-	μC
Peak Reverse Recovery Current	泄·新規部	see Fig.6 the not USE the	JINICE IDIPUL されないようお願 m for new design	-	36	-	Α
Thormal Characteristics	Note: Do						

■ Thermal Characteristics

Description	Symbol	min.	typ.	max.	Unit
Channel to Case	R _{th(ch-c)}	-	-	0.32	°C/W
Channel to Ambient	R _{th(ch-a)}	-	-	50	°C/W







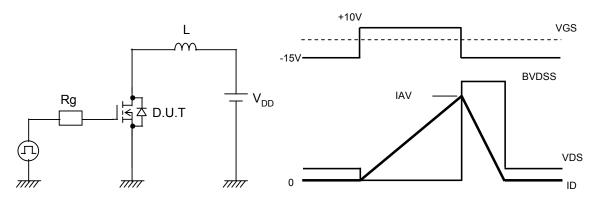


Fig.1 Avalanche Test circuit

Fig.2 Operating waveforms of Avalanche Test

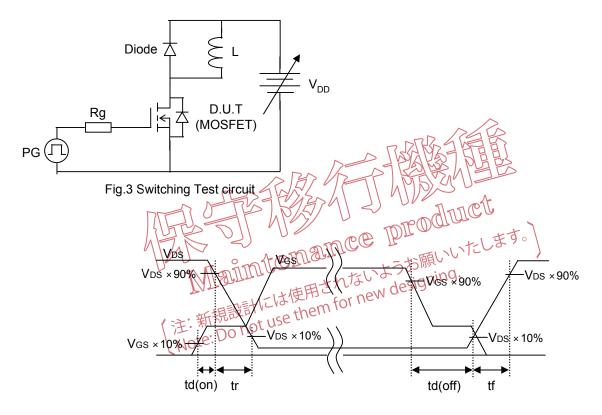


Fig.4 Operating waveform of Switching Test

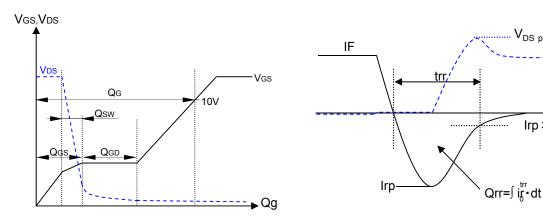


Fig.5 Operating waveform of Gate charge Test

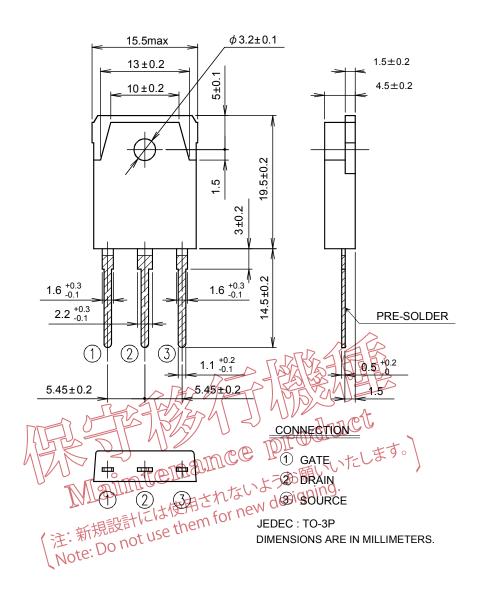
Fig.6 Operating waveform of Reverse recovery Test

 $V_{DS peak}$

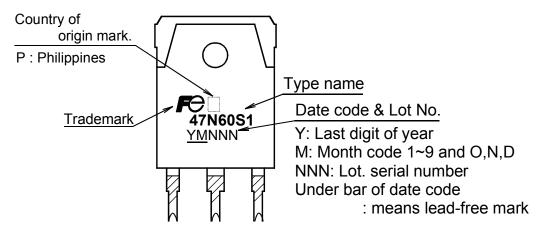
 V_{DS}

Irp × 10%

Outview: TO-3P Package



Marking



^{*} The font (font type, size) and the trademark-size might be actually different.

WARNING

1. This Catalog contains the product specifications, characteristics, data materials, and structures as of October 2015. The contents are subject to change without notice for specification changes or other reasons. When using a product listed in this Catalog, be sur to obtain the latest specifications

sur to obtain the latest specifications.

2. All applications described in this Catalog exemplify the use of Full's products for your reference only. No right or license, either express or implied, under any patent, copyright, trade secret or other intellectual property right owned by Fuji Electric Co., Ltd. is (or shall be deemed) granted. Fuji Electric Co., Ltd., makes no representation or warranty, whether express or implied, relating to the infringement or alleged infringement of other's intellectual property rights Which may arise from the use of the applications described herein.

3. Although Fuji Electric Co., Ltd. is enhancing product quality and reliability, a small percentage of semiconductor products may become faulty. When using Fuji Electric semiconductor products in your equipment, you are requested to take adequate safety measures to prevent the equipment from causing a physical injury, fire, or other problem if any of the products become faulty. It is recommended to make your design failsafe, flame retardant, and free of malfunction.

- 4. The products introduced in this Catalog are intended for use in the following electronic and electrical equipment which has normal reliability requirements.
- Computers
- · OA equipment
- Communications equipment (terminal devices)
- · Measurement equipment

- Machine tools
- Audiovisual equipment
- Electrical home appliances
- Personal equipment Industrial robots etc.
- 5. If you need to use a product in this Catalog for equipment requiring higher reliability than normal, such as for the equipment listed below, it is imperative to contact Fuji Electric Co., Ltd. to obtain prior approval. When using these products for such equipment, take adequate measures such as a backup system to prevent the equipment from malfunctioning even if a Fuji's product incorporated in the equipment becomes faulty.
- Transportation equipment (mounted on cars and ships)
- Traffic-signal control equipment
- Emergency equipment for responding to disasters and anti-burglary devices

- Trunk communications equipment
- Gas leakage detectors with an auto-shut-off feature
- · Safety devices
- 6. Do not use products in this Catalog for the equipment requiring strict reliability such as the following and equivalents to strategic equipment (without limitation).
- Space equipment • Submarine repeater equipment
- Aeronautic equipment
- · Nuclear control equipment
- 7. Copyright ©1996-2015 by Fuji Electric Co., Ltd. All rights reserved.

No part of this Catalog may be reproduced in any form or by any means without the express permission of Fuji Electric Co., Ltd.

8. If you have any question about any portion in this Catalog, ask Fuji Electric Co., Ltd. or its sales agents before using the product. Neither Fuji Electric Co., Ltd. nor its agents shall be liable for any injury caused by any use of the products not in accordance with instructions set forth herein.